

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

an interlayer insulating film disposed between said scanning lines and said signal lines;

a plurality of thin film transistors disposed at locations adjacent to intersections of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said plurality of thin film transistors;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of both said first and second substrates;

a first plurality of conductive layers interposed between said first substrate and said sealing member, said first plurality of conductive layers comprising a same material as said plurality of scanning lines;

a second plurality of conductive layers interposed between said first substrate and said sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines; and

an insulating film disposed between said first plurality of conductive layers and said second plurality of conductive layers

wherein said insulating film comprises a same material as said interlayer insulating film,

wherein said first plurality of conductive layers and said second plurality of conductive layers are arranged in turn so that said second plurality of conductive layers do not overlap said first plurality of conductive layers.

Please add the following new claims 33-76.

33. (New) The display device according to claim 1 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

34. (New) The display device according to claim 1 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

35. (New) The display device according to claim 1 wherein said first plurality of conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

36. (New) The display device according to claim 1 wherein said second plurality of conductive layers are electrically

isolated from either of said plurality of scanning lines and said plurality of signal lines.

37. (New) A display device comprising:

a first substrate having at least one side edge;

a plurality of first conductive lines extending over the first substrate in a first direction;

a plurality of second conductive lines extending over the first substrate in a second direction orthogonal to said first direction;

an interlayer insulating film disposed between said first conductive lines and said second conductive lines;

a plurality of thin film transistors disposed at locations adjacent to intersections of said first conductive lines and said second conductive lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

a second substrate located separated from said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a conductive layer comprising a same material as said plurality of second lines and interposed between said portion of the sealing member and said first substrate;

wherein said conductive layer continuously extends along said side edge for a length longer than a pitch of said second lines.

38. (New) The display device according to claim 37 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

39. (New) The display device according to claim 37 wherein each channel region of each of said plurality of thin film transistors has a crystalline structure.

40. (New) The display device according to claim 37 wherein said conductive layer is electrically isolated from either of said plurality of first conductive lines and said plurality of second conductive lines.

41. (New) The display device according to claim 37 wherein said conductive layer extends in a form of a rectangular wave.

42. (New) A display device comprising:

a first substrate having at least one side edge;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

an interlayer insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a conductive layer interposed between said portion of the sealing member and said first substrate, said conductive layer comprising a same material as said plurality of scanning lines,

wherein said conductive layer continuously extends along said side edge for a length longer than a pitch of adjacent ones of said scanning lines.

43. (New) The display device according to claim 42 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

44. (New) The display device according to claim 42 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

45. (New) The display device according to claim 42 wherein said conductive layer is electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

46. (New) The display device according to claim 42 wherein said conductive layer extends in a form of a rectangular wave.

47. (New) A display device comprising:
a first substrate;
a plurality of scanning lines extending over the first substrate in a first direction;
a plurality of signal lines extending over the first substrate in a second direction;
a plurality of thin film transistors disposed adjacent to each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

an interlayer insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates, wherein said plurality of scanning lines extend below and beyond a portion of said sealing member;

a plurality of conductive layers interposed between said first substrate and said portion of the sealing member, said plurality of conductive layers comprising a same material as said plurality of signal lines;

wherein said plurality of conductive layers are disposed between said plurality of scanning lines and below said portion of the sealing member in a location such that said plurality of conductive layers do not overlap said plurality of scanning lines.

46. (New) The display device according to claim 47 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

49. (New) The display device according to claim 47 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

50. (New) The display device according to claim 47 wherein said conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

51. (New) A display device comprising:

a first substrate having a first pair of opposed edges and a second pair of opposed side edges;

a plurality of scanning lines extending over the first substrate in a first direction along said first pair of opposed side edges;

an interlayer insulating film formed over said plurality of scanning lines;

a plurality of signal lines extending over interlayer insulating film in a second direction along said second pair of opposed edges;

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion located adjacent to one of said first pair of opposed side edges;

a first plurality of conductive layers comprising a same material as said scanning lines and interposed between said first substrate and below said portion of the sealing member, wherein said interlayer insulating film covers said first plurality of conductive layers;

a second plurality of conductive layers interposed between said interlayer insulating film and said portion of the sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines,

wherein said plurality of signal lines are electrically connected to said first plurality of conductive layers, respectively while second plurality of conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

52. (New) The display device according to claim 51 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

53. (New) The display device according to claim 51 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

54. (New) A display device comprising:

a first substrate;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of first thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said first thin film transistors;

an interlayer insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates;

a driver circuit comprising at least one second thin film transistor formed over said first substrate, said driver circuit disposed within a region surrounded by said sealing member;

a first plurality of conductive layers interposed between said first substrate and said sealing member, said first

plurality of conductive layers comprising a same material as said plurality of scanning lines;

a second plurality of conductive layers interposed between said first substrate and said sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines; and

an insulating film disposed between said first plurality of conductive layers and said second plurality of conductive layers wherein said insulating film comprises a same material as said interlayer insulating film,

wherein said first plurality of conductive layers and said second plurality of conductive layers are arranged in turn so that said second plurality of conductive layers do not overlap said first plurality of conductive layers.

55. (New) The display device according to claim 54 wherein each of said plurality of first thin film transistors is a top-gate type thin film transistor.

56. (New) The display device according to claim 54 wherein each channel region of said plurality of first thin film transistors has a crystalline structure.

57. (New) The display device according to claim 54 wherein said first plurality of conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

58. (New) The display device according to claim 54 wherein said second plurality of conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

59. (New) A display device comprising:

a first substrate having at least one side edge;

a plurality of first conductive lines extending over the first substrate in a first direction;

a plurality of second conductive lines extending over the first substrate in a second direction orthogonal to said first direction;

a plurality of first thin film transistors disposed at each intersection of said first conductive lines and said second conductive lines;

a plurality of pixel electrodes electrically connected to said first thin film transistors;

an interlayer insulating film disposed between said first conductive lines and said second conductive lines;

a second substrate opposed to said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a driver circuit comprising at least one second thin film transistor formed over said first substrate, said driver circuit disposed within a region surrounded by said sealing member; and

a conductive layer comprising a same material as said plurality of second lines and interposed between said portion of the sealing member and said first substrate;

wherein said conductive layer continuously extends along said side edge for a length longer than a pitch of said second lines.

60. (New) The display device according to claim 59 wherein each of said plurality of first thin film transistors is a top-gate type thin film transistor.

61. (New) The display device according to claim 59 wherein each channel region of said plurality of first thin film transistors has a crystalline structure.

62. (New) The display device according to claim 59 wherein said conductive layer is electrically isolated from either of

said plurality of first conductive lines and said plurality of second conductive lines.

63. (New) The display device according to claim 59 wherein said conductive layer extends in a form of a rectangular wave.

64. (New) A display device comprising:

- a first substrate having at least one side edge;
- a plurality of scanning lines extending over the first substrate in a first direction;
- a plurality of signal lines extending over the first substrate in a second direction;
- a plurality of first thin film transistors disposed at each intersection of said scanning lines and said signal lines;
- a plurality of pixel electrodes electrically connected to said first thin film transistors;
- an interlayer insulating film disposed between said scanning lines and said signal lines;
- a second substrate opposed to said first substrate;
- a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a driver circuit comprising at least one second thin film transistor formed over said first substrate, said driver circuit disposed within a region surrounded by said sealing member;

a conductive layer interposed between said portion of the sealing member and said first substrate, said conductive layer comprising a same material as said plurality of scanning lines,

wherein said conductive layer continuously extends along said side edge for a length longer than a pitch of the adjacent ones of said scanning lines.

65. (New) The display device according to claim 64 wherein each of said plurality of first thin film transistors is a top-gate type thin film transistor.

66. (New) The display device according to claim 64 wherein each channel region of said plurality of first thin film transistors has a crystalline structure.

67. (New) The display device according to claim 64 wherein said conductive layer is electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

68. (New) The display device according to claim 64 wherein said conductive layer extends in a form of a rectangular wave.

69. (New) A display device comprising:

a first substrate;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

an interlayer insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates wherein said plurality of scanning lines extend below and beyond a portion of said sealing member;

a driver circuit comprising at least one second thin film transistor formed over said first substrate, said driver circuit disposed within a region surrounded by said sealing member;

a plurality of conductive layers interposed between said first substrate and said portion of the sealing member, said

plurality of conductive layers comprising a same material as said plurality of signal lines;

wherein said plurality of conductive layers are disposed between said plurality of scanning lines below said portion of the sealing member.

70. (New) The display device according to claim 69 wherein each of said plurality of first thin film transistors is a top-gate type thin film transistor.

71. (New) The display device according to claim 69 wherein each channel region of said plurality of first thin film transistors has a crystalline structure.

72. (New) The display device according to claim 69 wherein said conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

73. (New) A display device comprising:

a first substrate having a first pair of opposed side edges and a second pair of opposed side edges;

a plurality of scanning lines extending over the first substrate in a first direction along said first pair of opposed side edges;

an interlayer insulating film formed over said plurality of scanning lines;

a plurality of signal lines extending over interlayer insulating film in a second direction along said second pair of opposed side edges;

a plurality of first thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said first thin film transistors;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion located adjacent to one of said first pair of opposed side edges;

a driver circuit comprising at least one second thin film transistor formed over said first substrate, said driver circuit disposed within a region surrounded by said sealing member;

a first plurality of conductive layers comprising a same material as said scanning lines and interposed between said first substrate and said portion of the sealing member, wherein said interlayer insulating film covers said first plurality of conductive layers;

a second plurality of conductive layers interposed between said interlayer insulating film and said portion of the sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines,

wherein said plurality of signal lines are electrically connected to said first plurality of conductive layers, respectively.

74. (New) The display device according to claim 73 wherein said second plurality of conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

75. (New) The display device according to claim 73 wherein each of said plurality of first thin film transistors is a top-gate type thin film transistor.

76. (New) The display device according to claim 73 wherein each channel region of said plurality of first thin film transistors has a crystalline structure.

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested.